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relatively to the clock uplink or downlink transitions; cumulating in the corresponding zone the analysis results; determining on the basis of the accumulation whether the sampling clock frequency and/or phase needs to be modified or not. The invention is applicable to signals derived from graphics cards.

IN THE DRAWINGS:

Please replace the originally filed page 1 / 4, Figure 1 with the attached new page 1 / 4, Figure 1.

REMARKS

The title has been amended to conform with the translated title of the published application (WO 00/07324).

The specification has been amended to include the change in title and to include a reference to the priority applications.

The above amendments to the claims have been made to eliminate reference indicia, to eliminate the multiple dependencies and to meet the requirements of the United States.

To meet the requirements of the United States, the Abstract (as originally filed in the PCT application) is added.

Page 1 / 4, Figure 1 of the Drawings has been amended as per the International Preliminary Examination Report.

No fee is believed to have been incurred by virtue of this amendment. However if a fee is incurred on the basis of this amendment, please charge such fee against deposit account 07-0832

Respectfully submitted,  
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